Docket No.: 50432-703

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Qi XIANG

Confirmation Number: 2738

Application No.: 10/827,432

Group Art Unit: 2811

Filed: April 20, 2004

Examiner:

For:

CMOS DEVICES WITH BALANCED DRIVE CURRENTS BASED ON SiGe

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OFR Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Assignee address is incorrect. Attached is a copy of the Assignment document, which evidences that the Assignee address should read: SUNNYVALE, CA. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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Date: October 12, 2004



United States Patent and Trademark Office

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50432-703

McDermott, Will & Emery 600 13th Street, N.W. Washington, DC 20005-3096



CONFIRMATION NO. 2738

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Date Mailed: 09/03/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if

Applicant(s)

Qi Xiang, San Jose, CA;

Assignment For Published Patent Application

Advanced Micro Devices, Inc., Sunnyvalle, CA

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Domestic Priority data as claimed by applicant

This application is a DIV of 10/316,826 12/12/2002 PAT 6,734,527

Foreign Applications

If Required, Foreign Filing License Granted: 09/02/2004

The number of your priority application, to be used for filing abroad under the Paris Convention is, US10/827,432

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

CMOS devices with balanced drive currents based on SiGe

Preliminary Class

257

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Docket No.: 50432-371

<u>ASSIGNMENT</u>

WHEREAS I, Qi XIANG of San Jose, CA, have made a certain new and useful invention as set forth in an application for United States Letters Patent, entitled CMOS DEVICES WITH BALANCED DRIVE CURRENTS BASED ON SiGe, executed by me on the date of execution of this document, as shown below, and filed concurrently herewith;

AND WHEREAS, Advanced Micro Devices, Inc., a corporation of the State of Delaware and having an address of One AMD Place, P. O. Box 3453, Sunnyvale, California 94088-3453 is desirous of acquiring the entire right, title and interest in and to said invention and in and to any and all Letters Patent of the United States and foreign countries which may be obtained therefor;

NOW, THEREFORE, for good and valuable consideration, the receipt for and sufficiency of which is hereby acknowledged, I do hereby sell, assign, transfer and set over unto Advanced Micro Devices, Inc., its legal representatives, successors, and assigns, the entire right, title and interest in and to said invention as set forth in the above-mentioned application, including the right of priority and including any continuations, continuations-in-part, divisions, reissues, re-examinations or extensions thereof, and in and to any and all patents of the United States and foreign countries which may be issued for said invention;

UPON SAID CONSIDERATIONS, I hereby agree with the said assignee that I will not execute any writing or do any act whatsoever conflicting with these presents, and that I will, at any time upon request, without further or additional consideration but at the expense of said assignee, execute such additional assignments and other writings and do such additional acts as said assignee may deem necessary or desirable to perfect the assignee's enjoyment of this grant, and render all necessary assistance in making application for and obtaining original, divisional, continuations, continuations-in-part, reexamined, reissued, or extended Letters Patent of the United States or of any and all foreign countries on said invention, and in enforcing any rights or choses in action accruing as a result of such applications or patents, by giving testimony in any proceedings or transactions involving such applications or patents, and by executing preliminary statements and other affidavits, it being understood that the foregoing covenant and agreement shall bind, and inure to the benefit of the assigns and legal representatives of assignor and assignee;

AND I request the Commissioner for Patents and Trademarks to issue any Letters Patent of the United States which may be issued for said invention to said Advanced Micro Devices, Inc., its legal representatives, successors or assigns, as the sole owner of the entire right, title and interest in and to said patent and the invention covered thereby.

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